

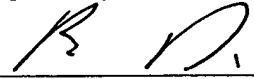
REMARKS

This Preliminary Amendment is requested prior to the initial examination of the above-identified patent application to address minor matters of form and syntax. No new matter has been added. If the Examiner has any suggestions for placing this application in even better form, the Examiner is invited to telephone the undersigned at the number listed below.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 18-0013, under Order No. SON-2802 from which the undersigned is authorized to draw.

Dated: March 10, 2006

Respectfully submitted,

By  47,255

Ronald P. Kananen

Registration No.: 24,104

RADER, FISHMAN & GRAUER PLLC

1233 20th Street, N.W., Suite 501

Washington, DC 20036

(202) 955-3750

Attorney for Applicant



APPENDIX I

Marked-up version of the Abstract is presented as shown below:

There is provided an information processing method characterized in that, in accordance with an instruction from a host CPU 411, either a CPU 103 or 104 loads a common code and an instruction code defined to be executed by itself from an external memory 110 into an internal memory 101, the other CPU loads an instruction code defined to be executed by itself from the external memory 110 into the internal memory 101, and then the respective CPUs load the respective instruction codes defined to be executed by themselves that are loaded in the internal memory 101, and ~~executing~~ execute the common code that is loaded in the internal memory 101 as required.